

ECR#: P24

Tracker #: 31

Status: Ratified

Title: I/O Connector Area Definition

Release Date: January 1999

Impact, Low: Motherboard

Spec Version: NLX Motherboard Specification V1.2

Summary:

The specification currently defines the back panel I/O shield opening. However, it does not define the allowable amount that the I/O connector can protrude from within the I/O shield opening. This ECR is intended to clarify this clearance to make sure an I/O shield can successfully be designed to function with the motherboard.

Changes Current Specification As Shown:

In Section 3.9.1, **add** the following text:

However, this specification does limit placement of the I/O connector to this extent: The motherboard supplier must ensure that there is sufficient clearance between the I/O connector, the connector housing, and the chassis to allow sufficient material in the I/O shield to make proper contact.

In Section 3.9.1, **replace** the existing figure with the modified figure provided here. Note: This figure also incorporates the changes recommended in ECR# P1.

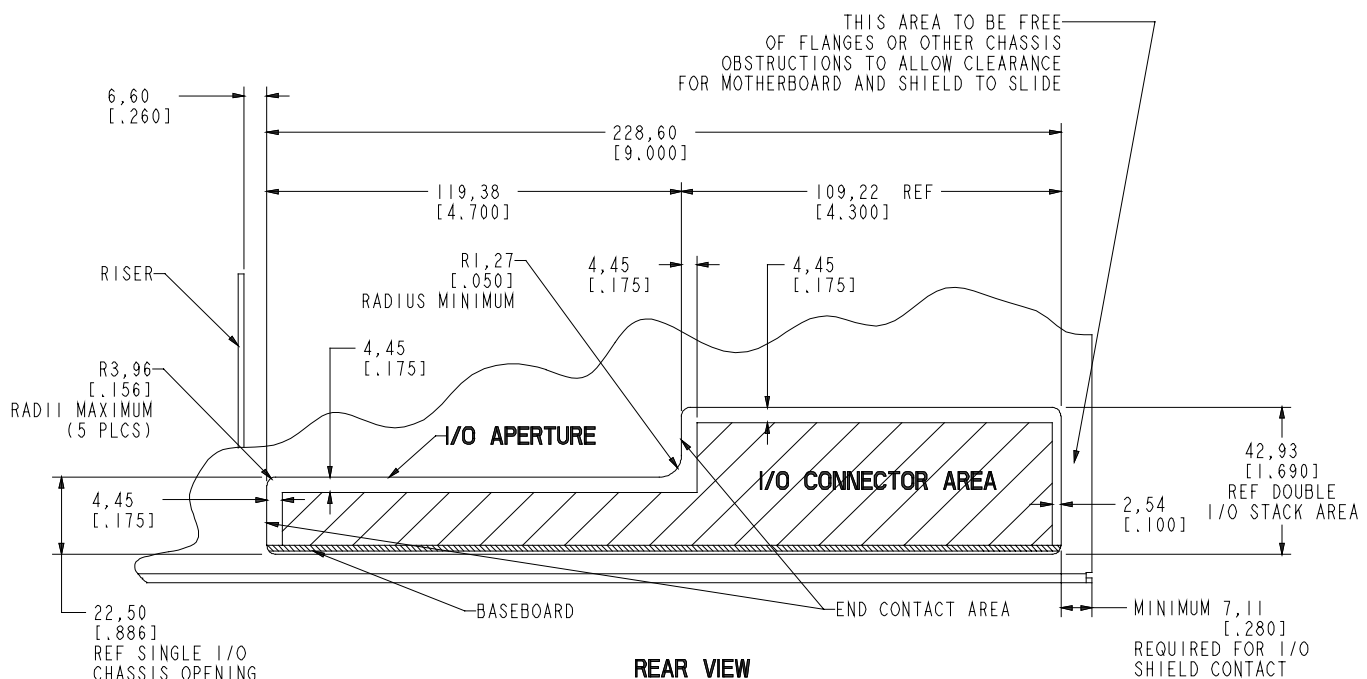


Figure 3.17: Chassis Back Panel I/O Shield Opening